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ATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yoshiyuki Nakamura **Examiner:** Unassigned
Serial No: Unassigned **Art Unit:** Unassigned
Filed: Herewith **Docket:** 14945
For: SEMICONDUCTOR APPARATUS **Dated:** September 27, 2001
FOR PROVIDING RELIABLE DATA
ANALYSYS OF SIGNALS

Assistant Commissioner for Patents
United States Patent and Trademark Office
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. Japanese Laid-Open Patent Application No. H6-201801, dated July 22, 1994;
2. Japanese Patent Gazette Application No. H7-31610, dated April 10, 1995;

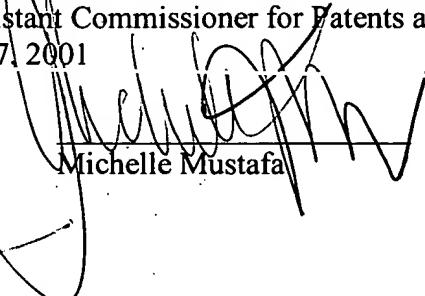
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I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents and Trademarks, Washington, D.C. 20231 on September 27, 2001

Dated: September 27, 2001


Michelle Mustafa

3. Japanese Laid-Open Patent Application No. H10-187554, dated July 21, 1998; and
4. Japanese Laid-Open Patent Application No. 2000-76894, dated March 14, 2000.

Applicant is submitting copies of the above-cited references. The relevance of the above-identified references has been described in the specification.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,


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